AMENDMENTS TO THE SPECIFICATION

Please replace paragraph 1, page 11 with the following paragraph:

The DS-CDMA inter-base station asynchronous cellular scheme used in the present invention is comprised of:

a first stage constituted by a long code timing detecting section 1 for detecting a long code timing and a threshold determination section 2 for determining a threshold by comparing the correlation value obtained by the long code timing detecting section 1 with an arbitrary threshold;

a second stage constituted by a long code group detecting section 3 for detecting a long code ~~timing~~ group and a threshold determination section 4 for determining a threshold by comparing the correlation value obtained by the long code group detecting section 3 with an arbitrary threshold; and

a third stage constituted by a long code detecting section 5 for detecting a long code from long code candidates, a threshold determination section 6 for determining a threshold by comparing the correlation value obtained by the long code detecting section 5 with an arbitrary threshold, and a frame synchronization detecting section 7 for performing frame synchronization detection with a detected long code.

Please replace paragraph 2, page 14 with the following paragraph:

Fig. 4 is a block diagram showing an example of the arrangement of the first ~~state~~ stage in the embodiment shown in Fig. 3.

Please replace paragraph 3, page 14 (which bridges over to page 15) with the following paragraph:

The first stage is comprised of a correlator 21 for detecting with a common identification code the correlation values of a real-part amplitude value (I) and imaginary-part amplitude value (Q), which are received by an antenna, quadrature-demodulated by a radio section, and A/D-converted in units of 1/2 chips, a correlation power value detecting section 22 for obtaining the correlation power values between the respective correlation output values detected by the correlator 21, an inter-frame averaging section 23 for averaging the correlation power values from the correlation power value detecting section 22 between the respective frames, a memory section 24 for holding the average correlation power value obtained by the inter-frame averaging section 23, a threshold decision section 25 for deciding a threshold by using the correlation power value held in the memory section 24, a determination section 26 for determining the correlation power value held in the memory section 24 by using the threshold decided by the threshold decision section 25, and a long code timing detecting section 27 for, if it is determined that an average correlation power value exceeds the threshold value, detecting a long code timing and notifying the second stage processing section of the long code timing.

Please replace paragraph 2, page 14 with the following paragraph:

The determination section 26 shown in Fig. 4 determines the decided threshold value. If the maximum correlation power value exceeds the threshold, the timing of the maximum correlation power value is set as a long code timing, and the long code timing detecting section 27 notifies the second stage processing section of the long code timing. If the maximum correlation power value does not exceed the threshold value, the processing in the first stage is performed again.

Please replace paragraph 5, page 17 (which bridges over to page 18) with the following paragraph:

The second stage is comprised of a correlator 41 for detecting at the long code timing notified from the first stage the correlation values between each long code group identification short code and the real-part amplitude value (I) and imaginary-part amplitude value (Q), which are received by the antenna, quadrature-demodulated by the radio section, and A/D-converted in units of 1/2 chips, a correlation power value detecting section 42 for obtaining correlation power values from the respective correlation output values detected by the correlator 41, an inter-frame averaging section 43 for averaging the correlation power values from the correlation power value detecting section 42 between frames, a memory section 44 for holding the average correlation power value obtained by the inter-frame averaging section 43, a long code group correlation sum detecting section 45 for obtaining the sum of the correlation power values held in the memory section 44 according to a transmission pattern based on each predetermined long code group identification short code, a determination section 46 for determining an arbitrary threshold from

each correlation sum detected by the long code group correlation sum detecting section 45, and determining a threshold, and a long code timing detecting section 47 for, when the determination section 46 determines that the maximum correlation sum exceeds the threshold, detecting a frame timing as a group to which the long code belongs.

Please replace paragraph 2, page 20 with the following paragraph:

The determination section 46 shown in Fig. 6 performs determination with the determined threshold. If the maximum correlation sum exceeds the threshold, the long code group detecting section 47 notifies the third stage of the long code group number and frame timing. If the maximum correlation sum does not exceed the threshold, the processing is performed again from the first stage.

Please delete the present Abstract of the Disclosure and replace it with the following new Abstract of the Disclosure.

A DS-CDMA inter-base station asynchronous cellular scheme including three stages. The first stage detects a long code timing and determines a threshold. The second stage detects a long code group and determines a threshold. The third stage detects a long code from long code candidates determines a threshold and performs frame synchronization detection with a detected long code.